



SUGGESTED PAD LAYOUT

ELECTRICAL CHARACTERISTICS :

1. INSULATION HI-POT : 5K Vrms/0.5mA ,1 Sec.
2. TURN RATIO (±5%) : 1CT : 1CT
3. OCL : 350uH MIN. 8mA/DC Bias
4. CROSS TALK (dB MIN.) : 30MHz ; -45, 60MHz ; -40, 100MHz ; -35
5. INSERTION LOSS (1MHz-100MHz) : 1.0dB MAX.
6. RETURN LOSS (dB MIN. @100 Ω) :

1-30MHz	40MHz	50MHz	60-80MHz	100MHz
-18	-14.4	-13.1	-12	-10
7. DIFFERENTIAL TO COMMON MODE REJECTION (dB MIN.) :

30MHz	60MHz	80MHz
-43	-37	-33
8. OPERATING TEMPERATURE RANGE : -40°C~+85°C
9. RoHS COMPLIANT

					AOEM.		TITLE 10/100 BASE-T TRANSFORMER	
							DWG. NO. ATPL-445R	
RELEASE					UNITS:		SAFETY	
							SHEET 1 OF 1	
NO:	DESCRIPTION	DATE	BY	CHK	APPD	DATE	P/N:	DRAW
REVISIONS								